



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Donald Malcolm MacIntyre

Application No.: 09/045,507

Filed: March 20, 1998

For: WAFER SCALE SEMICONDUCTOR

**STRUCTURE** 

Commissioner for Patents MAIL STOP PGPUB

Alexandria, VA 22313-1450

P.O. Box 1450

Confirmation No.: 1010

Group Art Unit: 2814

Examiner: Wille, Douglas A

## SUBMISSION OF REPLACEMENT FORMAL DRAWINGS

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## **CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 27, 2005.

STALLMAN & POLLOCK LLP

Dated: 06/27/05

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Sir:

Applicant encloses three (3) sheets of formal drawings corresponding identically to the informal drawings filed with the above-identified U.S. patent application. Applicant requests that the formal drawings be substituted for the original-filed informal drawings.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: June 27, 2005

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